## Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination | MENUT ET AL. | Examiner | Art Unit | Page 1 of 1

## U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6,580,123	06-2003	Thapar	257/330
	В	US-6,171,923	01-2001	Chi et al.	438/386
	C	US-5,543,348	08-1996	Hammerl et al.	438/249
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	Η	US-			
	ı	US-			
	J	US-			
	K	US-			
	L	US-			
	М	US-	·		

## FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q					·
	R		·			
	s					
	Т					

## **NON-PATENT DOCUMENTS**

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
	U	"One-Device CMOS DRAM Cell With Buried Poly Trench and Drain Reach Through", April 1988, IBM Tech. Dis. Bull. (ITDB), Vol. 30, No. 11, p. 451-2				
Ħ	v					
	w					
	х					

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.